

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/660,548	09/12/2003	Ying-Yi Fan	JA-GAR/US 29	6426
759	7590 06/27/2005		EXAMINER	
Johnson & Associates, PC			BUI, HUNG S	
# 282 14625 Baltimore Avenue		ART UNIT	PAPER NUMBER	
Laurel, MD 20707			2841	
		DATE MAILED: 06/27/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
Office Action Summary		10/660,548	FAN ET AL.			
		Examiner	Art Unit			
		Hung S. Bui	2841			
	The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1)	Responsive to communication(s) filed on	_,				
2a) <u></u>		action is non-final.				
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Dispositi	on of Claims					
5)□ 6)⊠ 7)□	4) Claim(s) 1-6 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 1-6 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement.					
Applicati	on Papers					
10)⊠	The specification is objected to by the Examine The drawing(s) filed on <u>09/12/2003</u> is/are: a) Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Ex] accepted or b)⊠ objected to by drawing(s) be held in abeyance. See ion is required if the drawing(s) is obj	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).			
Priority u	ınder 35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
Attachment(s)						
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413)						
3) 🔲 Inform	e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date	Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	ate atent Application (PTO-152)			

DETAILED ACTION

Drawings

1. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(4) because reference character "2" has been used to designate both the wiring isolation plate and control circuit board. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. Each drawing sheet submitted after the filling date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. ClaimS 1 and 3-5 are rejected under 35 U.S.C. 103(a) as being unpatentable over Crane et al. [US 5,899,761] in view of Spitaels [US 6,775,715].

Regarding claim 1, Crane et al. disclose an power strip assembly (10) comprising a face panel (48, figure 1), an upper cover (12), at least one socket member (66), a

Application/Control Number: 10/660,548

Art Unit: 2841

wiring insulation plate (18), a detachable lower cover (14) and a power cord lead (20), wherein the upper and lower covers are connected via several electrical wires (figure 1).

Crane et al. disclose everything claimed except for the power strip having an uninterruptible power supply socket, a battery set and a printed circuit board controller.

Spitaels discloses a circuit (4) an uninterruptible power supply including a battery set (18) and controller (54).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to use the circuit of Spiteals, with the power strip assembly of Crane et al., in order to provide backup power.

Regarding claim 5, Crane et al. disclose everything claimed except for the use of a plurality of surge suppression outlets.

Spitaels further disclose at least one surge suppression circuit (12) and outlet (26).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to add additional outlets in order to accommodate multiple peripherals.

4. Claims 2 and 6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Crane et al., as modified, as applied to claim 1 above, and further in view of Heidorn et al. [US 5,606,489].

Regarding claims 2 and 6, Crane et al., as modified, disclose everything claimed except for the use of multiple circuit boards being arranged perpendicular relative to a power circuit board.

Heidorn et al. disclose a fault detection board (68) being mounted perpendicularly to a power circuit board (56).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to use the circuit board arrangement of Heidorn et al. for fault detection boards of Crane et al., as modified, in order to facilitate replacement of a faulty detection board.

To use a plurality of fault detection boards with the power circuit board of Crane et al., as modified, would have been obvious in order to enable the power circuit board to detect multiple types of faults.

5. Claims 3-4 are rejected under 35 U.S.C. 103(a) as being unpatentable over Crane et al., as modified, as applied to claim 1 above, and further in view of Ho [US 6,650,027].

Regarding claims 3-4, Spitaels further disclose the UPS circuit including a AC/DC rectifier/battery charger (20), a DC/DC inverter (22), a switch controller (14), an output inverter (26) and a computer transmission interface circuit (40).

Crane et al., as modified, disclose everything claimed except for a DC/DC direct current bus booster.

Ho discloses a UPS circuit including a DC converter with a voltage booster component (column 1, lines 40-45).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to include a DC converter with a voltage booster with the circuit of

Application/Control Number: 10/660,548

Art Unit: 2841

Crane et al., as modified, as suggested by Ho, for the purpose of stepping up the output

voltage of the storage battery.

Conclusion

6. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Hung S. Bui whose telephone number is (571) 272-

2102. The examiner can normally be reached on Monday-Friday 8:30AM-6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Kamand Cuneo can be reached on (571) 272-1957. The fax phone number

for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

6/22/05

Hung Bui

Art unit 2841

Page 5